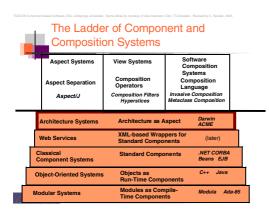


- C. Hofmeister, R. Nord, D. Soni. Applied Software Architecture Addison-Wesley, 2000. Very nice book on architectural elements in UML.
- Rikard Land: A Brief Survey of Software Architecture. MRTC report ISSN 1404-3041 ISRN MDH-MRTC-57/2002-1-SE, Mälardalen Real-Time Research Centre, Mälardalen University, February, 2002
- Martin Alt. On Parallel Compilation. PhD Dissertation, Universität des Saarlandes, Saarbrücken, Feb. 1997. (CoSy prototype)
- ACE b.V. Amsterdam. CoSy Compilers. System documentation, Apr. 2003. http://www.ace.nl

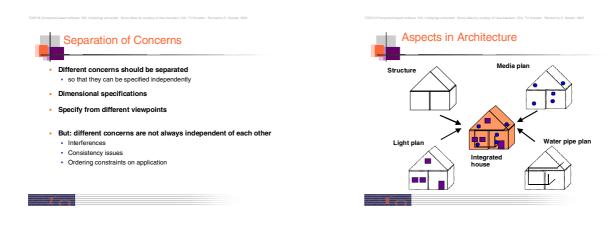
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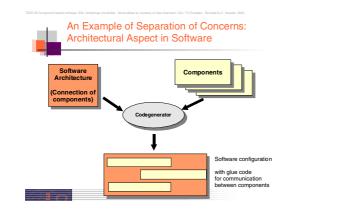
Gregory Zelesnik. The UniCon Language User Manual. School of Computer Science, Carnegie Mellon University Pittsburgh, Pennsylvania Gregory Zelesnik. The UniCon Language Reference Manual. School of Computer Science, Carnegie Mellon University Pittsburgh, Pennsylvania

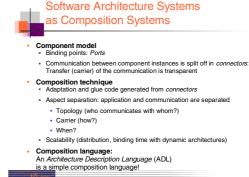
(Darwin) http://www-dse.doc.ic.ac.uk/Software/Darwin/

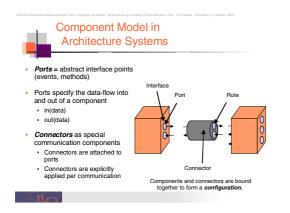


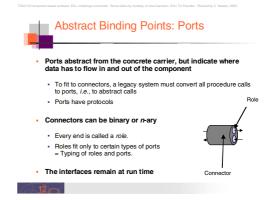
- A Basic Rule for Design ...
  - ... is to focus on one problem at a time and to forget about others.
  - Abstraction is neglection of unnecessary detail - Display and consider only essential information

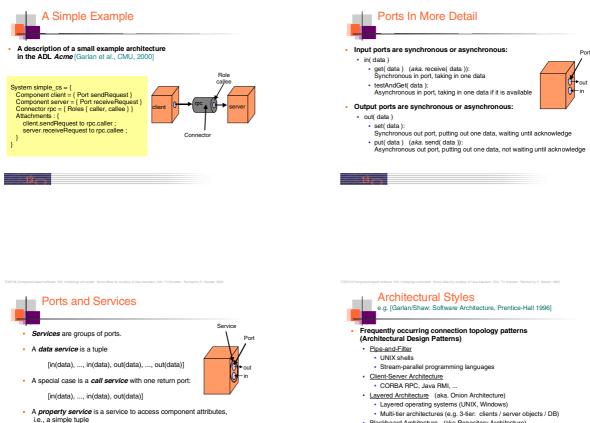






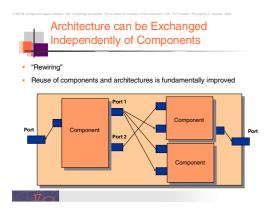


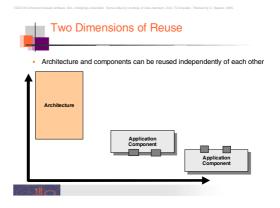


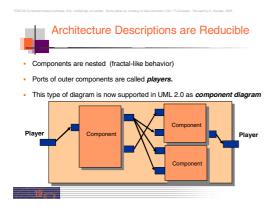


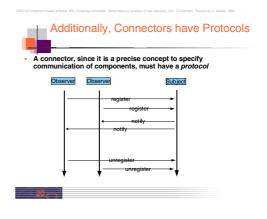
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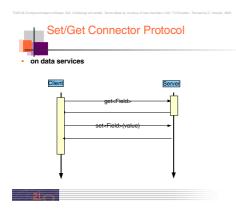
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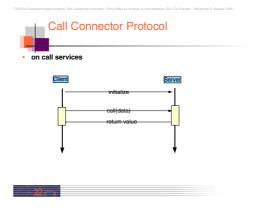


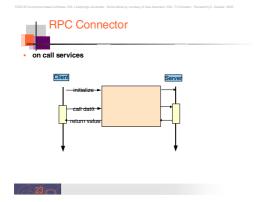


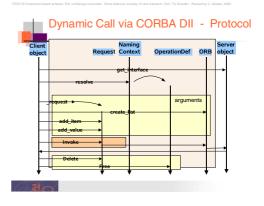


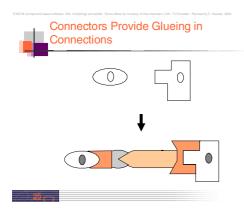


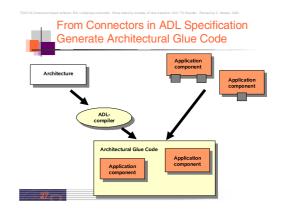


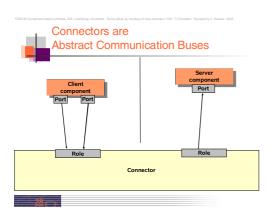


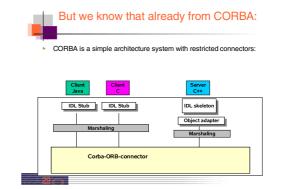


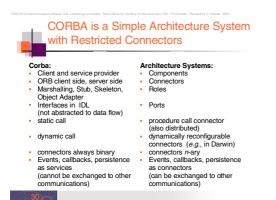


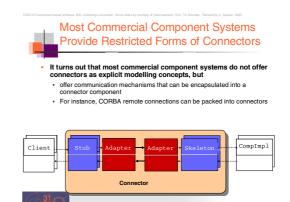


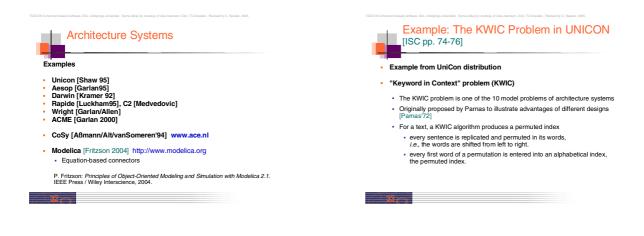


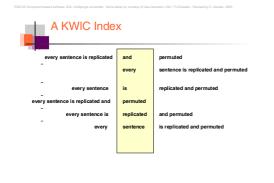


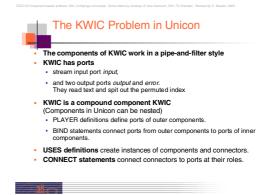


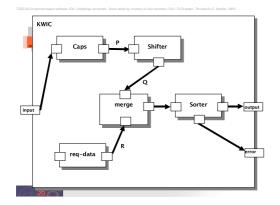


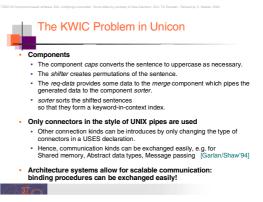




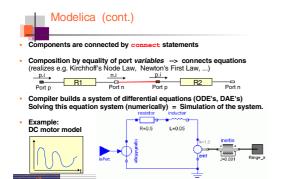


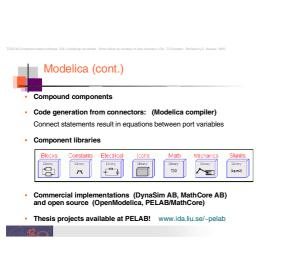


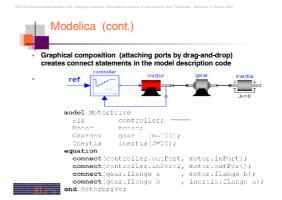


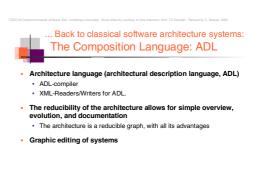


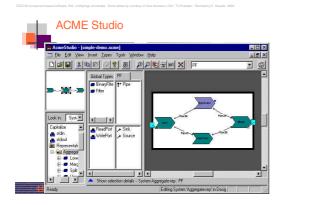
TIDD'E Compared based aufbases Dr. Lindigeng untreating. Bane allow by workey of blan Abaneen, DAV TUD basies. Noviembly C. Seaaks. 2005.		TOD'D Chrywyne Award Andre Sch, Lafange urwarde , Siene akady wraeg o'r Mae Anaron, Chriff Chrader, Roland yr C Maerie 2005	
COMPONENT KWIC /* This is the interface of KWIC with in- and output ports */ INTERFACE IS TYPE Filter PLAYER input IS Streamin SIGNATURE (filter)		<ul> <li>Equation-based language for modeling and simulation of systems in physics and engineering</li> </ul>	
PORTBINDING (stdin) END input	/* Here come the connections */	<ul> <li>Component model, ports, connectors</li> </ul>	
PLAYER output IS StreamOut SIGNATURE ("Ine") PORTBINDING (sidout) END output END INTERFACE IMPLEMENTATION IS /* Here come the component definitions // USES and INTERFACE upcase END cops USES sinter INTERFACE const-data END rec-data USES met and INTERFACE const-data END rec-data USES met ent INTERFACE const-data END rec-data	BIND input TO capsingut CONNECT association TO P asive CONNECT shifter august TO P asive CONNECT instructure CONNECT megalization CONNECT megalization CONNECT megalization CONNECT megalization CONNECT megalization Constructure Stratust White accentrations of EstRALISH this accentration	<ul> <li>Simple example: Resistor component</li> <li>pi Portp</li> <li>Resistor n</li> </ul>	
USES sofier INTERFACE sort END sofier /* Here come the connector definitions /* USES P PROTOCOL. Unix-pipe END P USES Q PROTOCOL. Unix-pipe END Q USES Q PROTOCOL. Unix-pipe END R	enrouezant on la presentation menge audput AS source sonter input AS sink END Unit-scipe BIND output TO sorter output END IMPLEMENTATION END KWIC	<pre>model Resistor "Ideal resistor"     extends OneFort;     parameter Resistance R; equation     R*p.i = v; end Sesistor;</pre>	













# Checking and Validating Checking, analysing Test of (part of) an architecture with dummy components Deadlock checking

- Liveness checking
- Validation: Tools for consistency of architectures
- Are all ports bound?Do all protocols in the connectors fit?
- Do all protocols in the connectors fit?
   Does the architecture correspond to a certain style ?
- · Does the architecture fit to a reference architecture?
- Parallelism features as deadlocks, fairness, liveness,

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· Dead parts of the systems: Is everything reachable at run time?

# What can be generated? Glue- and adapter code from connectors and ADL-specifications Mapping of the protocols of the components to each other Generation of glue code from the connectors Gluniations of architectures (with dummy components): The architecture can be created first And tested stand-alone

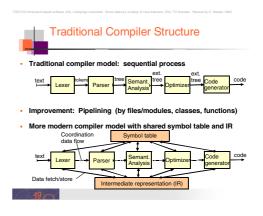
- · Run time estimates are possible (if run times of components are known)
- Test cases for architectures
- Documentation (graphic structure diagrams)

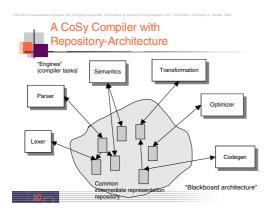
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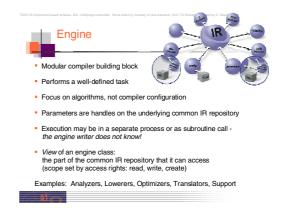


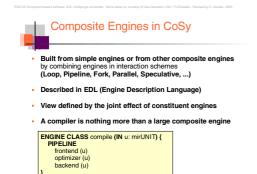
A commercial architecture system for compilers

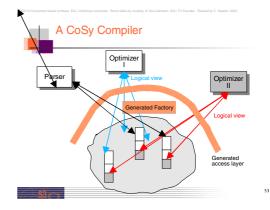
[ISC 1.3] www.ace.nl

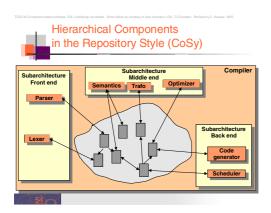


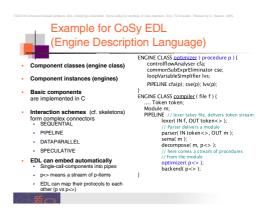


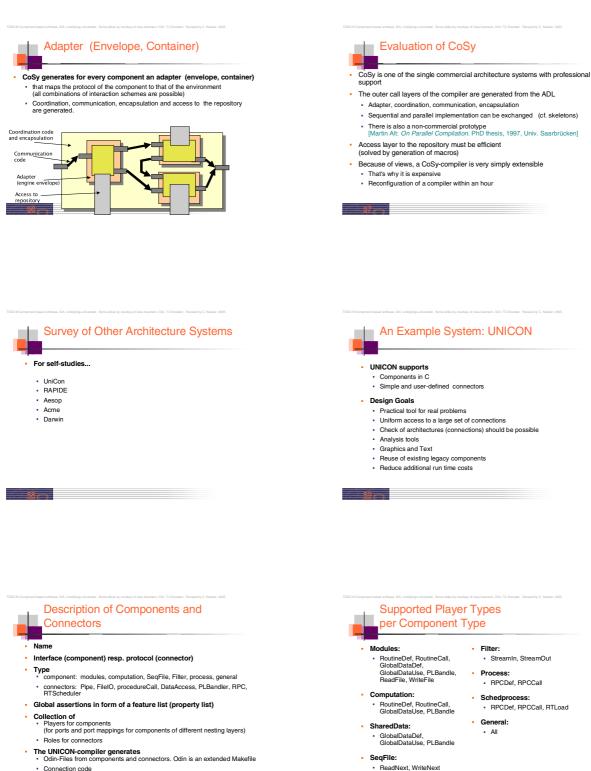




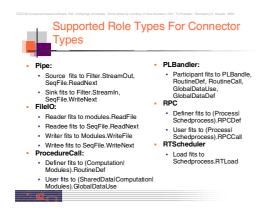


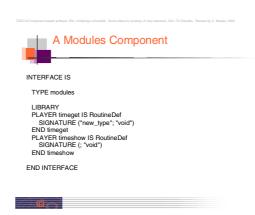


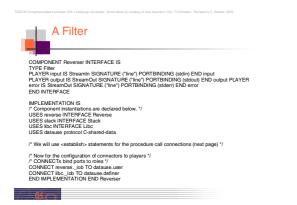




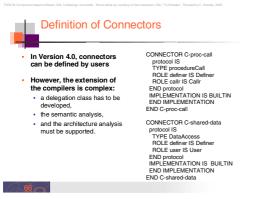
Connection code

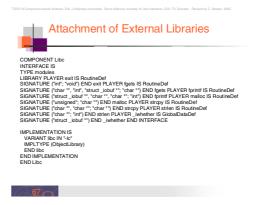




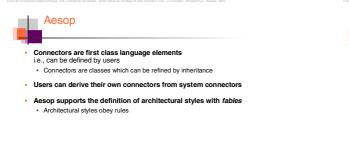


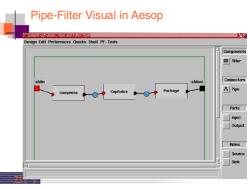






COMPONENT KWIC INTERFACE IS TYPE Flag. PLANET POOTBINDING (sain) END input PLAYER output IS StreamOut SIGNATURE ("Iner) PORTBINDING (sain) END angut PLAYER error'IS StreamOut SIGNATURE ("Iner) PORTBINDING (sain) END error SIGNATURE ("Iner) PORTBINDING (sain) END error	Luckham/Vera/Meldal. Three Concepts of System Architecture. Stanford University 1995.	
SIGNATURE ("line") PORTBINDING (stdout) END output PLAYER error IS StreamOut		
	<ul> <li>Central idea: Rapide leaves the object connection architecture, in which the objects are attached to each other directly, for an interface connection architecture, in which required and provided interfaces</li> </ul>	
NULSCREEN SZE (lie real-width 800. width-unit "-val-height 550 height-unit ")') DIRECTORY (lie s'unit annaheidh e ugasa unit "instreample-scheidh unit" 'userkample's data uni "userkamples unit "userkamples unit annaheidh unit ")'	are related to each other • Specify in a interface not only the required methods, but also the offered ones (provided and required ports)	
//usr/examples/ reverse-f.uni*/) USES cass INTERFACE upcase	<ul> <li>Connect the ports in a architecture description (separate)</li> </ul>	
GUI-SCREEN-POSITION ("(lis :position (@pos 68 123) :player-positions (lis (cons "input" (cons 'left 0.5)) (cons "error" (cons `right 0.6625))	<ul> <li>Advantage: calls can be bound to other ports with different names</li> </ul>	
(cons 'output' (cons 'right 0.3375))))') END caps (remaining definition owithted)	<ul> <li>Generalizes ports to calls</li> </ul>	
END IMPLEMENTATION END KWIC	<ul> <li>Fundamentally more flexible concept for modules!</li> <li>Rapide was marketed by a start-up company</li> </ul>	
	<b>69</b> 0	





Aesop Supports Architectural Styles (Fables)

#### Design Rule

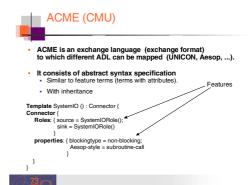
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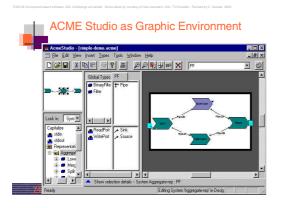
- A design rule is an element of code with which a class extends a method of a super class. A design rule consists of the following:
   A pre-check that helps control whether the method should be run or not.
   A post-action

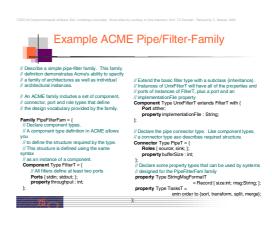
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- Environment A design environment tailored to a particular architectural style.

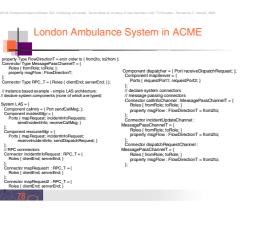
  - It includes a set of policies about the style, and a set of tools that work in harmony with the style, visualization information for tools
     If something is part of the formal meaning, it should be part of a style
  - If it is part of the presentation to the user, it should be part of the environment.

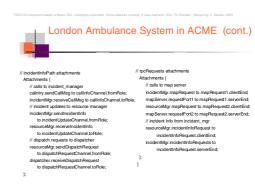




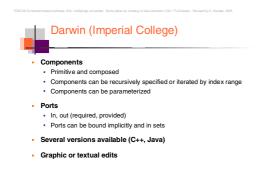


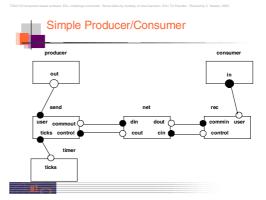


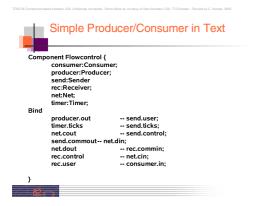




### A.79







#### Architecture Languages versus UML

- So far, architecture systems and languages were research toys (except CoSy)
- "I have to learn UML anyway, should I also learn an ADL??" · Learning curve for the standard developer
  - Standard? Development environments?
- This changes with UML 2.0

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Hofmeister, Nord, Soni: Describing Software Architecture with UML. 1999

## The Hofmeister Model of Architecture

# [Hofmeister/Nord/Soni'99] is the first article that has propagated the idea of specifying an architecture language with UML

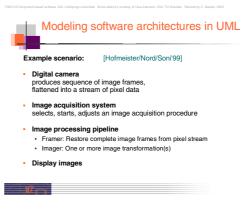
- · Conceptual view: Functionality + interaction (components, ports, connectors)
- Module view: Layering, modules and their interconnections
- Execution view: runtime architecture (mapping modules to time and resources)
- · Code view: division of systems into files

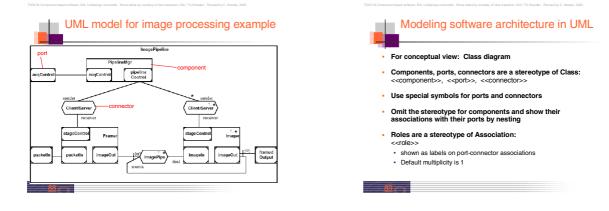
#### Describe these single views in UML

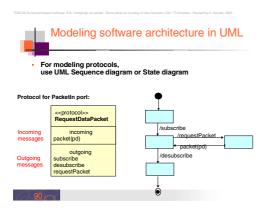
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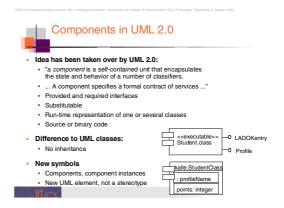
- · UML allows the definition of stereotypes
  - Model connectors and ports, modules, runtime components with stereotypes Map them to icons, so that the UML specification looks similar to a specification in a architecture system

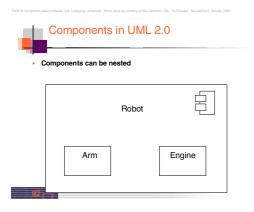
TDDC18 Con	TDDC18 Component-based software. IDA, Linköpings universitet. Some sildes by countracy of Uwe Assmann, IDA / TU Dreaden. Revised by C. Kessler, 2005.							
Background: Stereotypes in UML								
<ul> <li>A stereotype is a UML modeling element introduced at modeling time. It represents a subclass of an existing modeling element (-&gt;metalevel) with the same form (attributes and relationships) but with a different intent, maybe special constraints.</li> </ul>								
	< <person>&gt; Student</person>	< <person>&gt; Student</person>						
	someMethod()	someMethod	Student					
<ul> <li>To permit limited graphical extension of the UML notation as well, a graphic icon or a graphic marker (such as texture or color) can be associated with a stereotype.</li> </ul>								
<ul> <li>A mechanism for extending/customizing UML without changing it.</li> </ul>								
[UML Notation Guide, 1997]     (Control of the second								

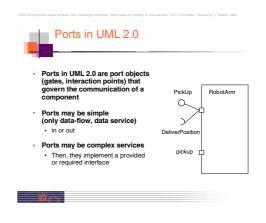


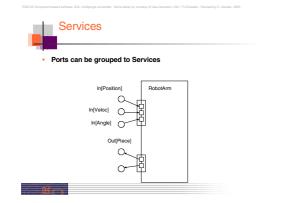


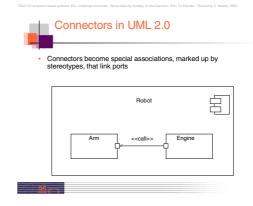


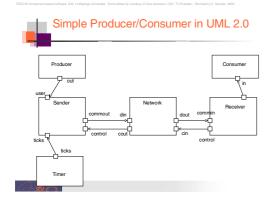


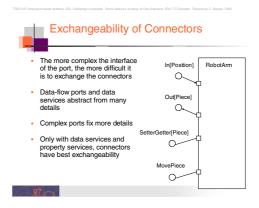


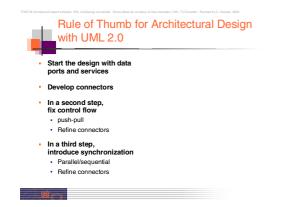








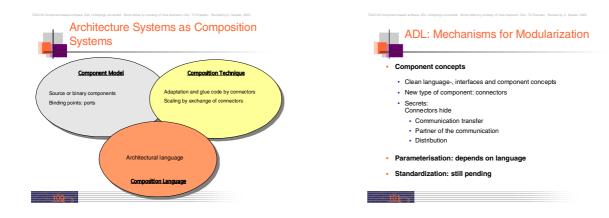


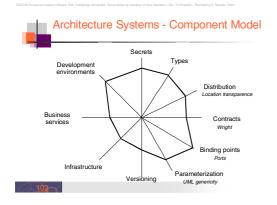


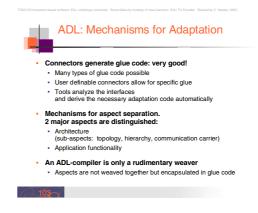


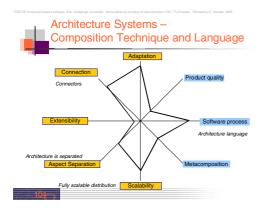
How to evaluate architecture systems as composition systems?
 Component model

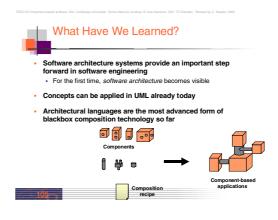
- Composition technique
- Composition language













- The attempts to describe architecture concepts with UML are promising
   Model-driven architecture
- Increasingly popular, also in embedded / realtime domain
- We should think more about general software composition mechanisms
  - Adaptation by glue is only a simple way of composing components (... see invasive composition)

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