

# DF00100

## Advanced Compiler Construction

### VT1/2010

## Exercise Set 1

### Exercise 1.1

Given the following C module:

```
#define NLIMIT 128
int A[NLIMIT];
int ntop = 0; // largest tabulated A entry so far
extern void errmsg( void );

int f( int n )
{
    int i;
    if (n<1 || n>=NLIMIT) {
        errmsg();
        return 0;
    }
    if (n>ntop) {
        for (i=ntop+1; i<=n; i++)
            A[i] = A[i-1] + i;
        ntop = n;
    }
    return A[n];
}
```

1. Give the symbol table of the program and the abstract syntax tree of function **f**.  
Use the notation of the slides and complement with your own notation where necessary.
2. Give a HIR representation of function **f**.  
(Hint: You can use short-circuit evaluation of the `||` operator).
3. Give a MIR representation of function **f**.
4. Give a LIR representation of function **f**.
5. Identify the basic block leaders in the MIR representation of **f** and draw the basic block graph and the flow graph of **f**. Assume here for simplicity that function calls do not delimit a basic block.
6. Compute the dominators and immediate dominators for all nodes in the flow graph of **f**, using the iterative algorithm. Draw the dominator tree of function **f**.

### Exercise 1.2

Given the following basic block in MIR code:

```
c ← a + 1
b ← 2 * a
b ← b + c
d ← c
a ← b / d
d ← b / c
c ← a - b
a ← 2 * a
```

Perform local common subexpression elimination to obtain a DAG representation of the code.

### Exercise 1.3

We visualized the *dominance* relation on the slides with the light flow analogy. What would be the corresponding setup for the *postdominance* relation?

Recall that the postdominance relation is defined as follows:

$p$  *postdominates*  $b$  ( $p$  pdom  $b$ ) if every possible execution path  $b \rightarrow^* \boxed{\text{exit}}$  includes  $p$ .

### Exercise 1.4

Given the following MIR code

```
    i ← From
    s ← 0
    t0 ← ( From ≤ To)
    if not t0 goto L0
L1:  t1 = i × Int_Size
    t2 ← A + t1
    t3 ← [t2] (memory read)
    t4 ← (a < 0)
    if t4 goto L2
    t5 ← a × 0.8
    t6 ← s + t5
    s ← t6
    goto L3
L2:  t7 ← a × 0.8
    t8 ← s - t7
    s ← t8
L3:  t9 ← i × Int_Size
    t10 ← E + t7
    [t10] ← s (memory write)
    t11 ← i + 1
    i ← t11
    t12 ← (i ≤ To)
    if t12 goto L1
L0:  return s
```

1. Draw the control flow graph, find the basic block leaders, and draw the basic block graph and the flow graph.
2. Compute the dominators of all nodes in the flow graph using the iterative algorithm.

Compute the immediate dominators and draw the dominator tree.

3. Do a DFS traversal of the flow graph, starting with the entry node, and label the edges with T, F, B and C.
4. Identify strongly connected components and natural loops in the code. Is the flow graph reducible? If yes, draw the region hierarchy graph, following the structural analysis transformations suggested in the lecture on control flow analysis.  
Give names to the variable definitions and expressions that occur in the code [Example:  $d_1$  denotes the first assignment to  $i$ , and  $e_1$  denotes the comparison (From  $\leq$  To)].
5. Formulate the flow functions and the dataflow equations for the Available Expressions problem for each basic block.
6. Give a trivial, but correct solution to the Available Expressions problem.
7. Determine a fixed-point solution of the Available Expressions problem with the worklist algorithm. Explain whether your solution is a maximal safe solution or not.
8. Formulate the flow functions and the dataflow equations for the Reaching Definitions (May-Reach) problem for each basic block.
9. Give a trivial, but correct solution to the Reaching Definitions problem.
10. Determine a fixed-point solution of the Reaching Definitions problem with the worklist algorithm. Explain whether your solution is a maximal (in the information-theoretic, not set-theoretic sense) safe solution or not.

Please prepare exercises 1.1–1.4.4 for the first lesson, exercises 1.4.4–1.4.10 for the second lesson.