



Lecture 14

Compiler Frameworks and Compiler Generators

A (non-exhaustive) survey

with a focus on open-source frameworks

Peter Fritzon, Christoph Kessler,
IDA, Linköpings universitet, 2010.

Compiler Generators or CWS - Compiler Writing Systems



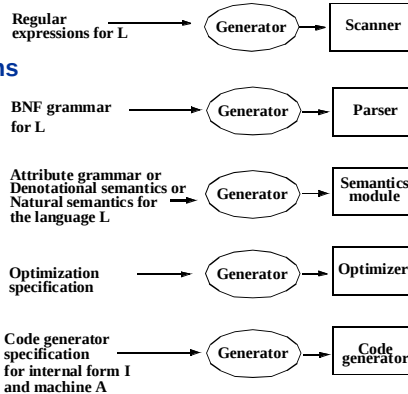
- A Compiler Generator or CWS is a program which, given a description of the source language and a description of the target language (object code description), produces a compiler for the source language as output.
- Different generators within CWS generate different phases of the compiler.

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Compiler Generator Formalisms and generated Compiler Modules



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Syntax-Based Generators

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Syntax-Based Generators



- LEX or FLEX – generates lexical analysers
- YACC or BISON – generates parsers
 - Compiler components that are not generated:
 - semantic analysis and intermediate code gen.
 - the optimisation phase
 - code generators
- Note: YACC produces parsers which are bad at error management

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Semantics-Based Generators

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RML - A Compiler Generation System and Specification Language from Natural Semantics/Structured Operational Semantics

Goals

- Efficient code — comparable to hand-written compilers
- Simplicity — simple to learn and use
- Compatibility with "typical natural semantics/operational semantics" and with Standard ML

Properties

- Deterministic
- Separation of input and output arguments/results
- Statically strongly typed
- Polymorphic type inference
- Efficient compilation of pattern-matching

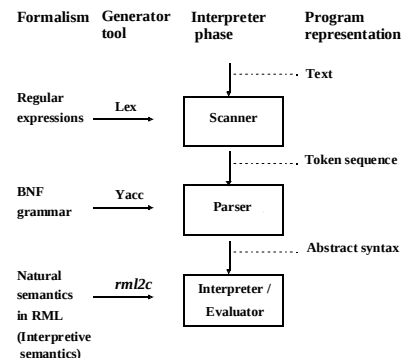
www.ida.liu.se/pelab/~rml

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Example Use: Generating an Interpreter Implemented in C, using rml2c

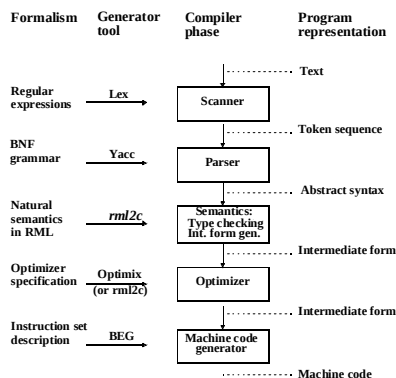


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Example Use: Generating a Compiler Implemented in C



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RML Syntax

- Goal: Eliminate plethora of special symbols usually found in Natural Semantics/Operational Semantics specifications

Software engineering viewpoint: identifiers are more readable in large specifications

- A Natural semantics/Operational semantics rule:

$$\frac{H1 \vdash T1 : R1 \dots Hn \vdash Tn : Rn}{H \vdash T : R} \text{ if } \langle \text{cond} \rangle$$

- Typical RML rule:

```
rule NameX(H1, T1) => R1 &
...
NameY(Hn, Tn) => Rn &
<cond>
-----
RelationName(H, T) => R
```

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Example: the Exp1 Expression Language

Typical expressions

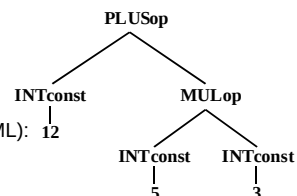
12 + 5*3
-5 * (10 - 4)

Abstract syntax (defined in RML):

datatype Exp

```
= INTconst of int
| PLUSop of Exp * Exp
| SUBop of Exp * Exp
| MULop of Exp * Exp
| DIVop of Exp * Exp
| NEGop of Exp
```

Abstract syntax tree of 12 + 5*3



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Evaluator for Exp1

relation eval: Exp => int =

Evaluation of an integer constant ival is the integer itself:

axiom eval(INTconst(ival)) => ival

Evaluation of an addition node PLUSop is v3,

if v3 is the result of adding the evaluated results of its children e1 and e2

Subtraction, multiplication, division operators have similar specifications.

(we have removed division below)

```
rule eval(e1) => v1 & eval(e2) => v2 &
int_add(v1,v2) => v3
-----
eval( PLUSop(e1,e2) ) => v3

rule eval(e1) => v1 & eval(e2) => v2 &
int_sub(v1,v2) => v3
-----
eval( SUBop(e1,e2) ) => v3

rule eval(e1) => v1 & eval(e2) => v2 &
int_mul(v1,v2) => v3
-----
eval( MULop(e1,e2) ) => v3

rule eval(e) => v1 & int_neg(v1) => v2
-----
eval( NEGop(e) ) => v2

end
```

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Simple Lookup in Environments Represented as Linked Lists

```

relation lookup: (Env, Ident) => Value =
(* lookup returns the value associated with an identifier.
   If no association is present, lookup will fail.
   Identifier id is found in the first pair of the list, and value is returned. *)

rule id = id2
-----
lookup((id2, value) :: _, id) => value

(* id is not found in the first pair of the list,
   and lookup will recursively search the rest of the list.
   If found, value is returned. *)

rule not id=id2 & lookup(rest, id) => value
-----
lookup((id2, _) :: rest, id) => value

end

```

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Translational Semantics of the PAM language – Abstract Syntax to Machine Code

PAM language example:

```

read x,y;
while x<= 99 do
  ans := (x+1) - (y / 2);
  write ans;
  read x,y
end

```

Simple Machine Instruction Set:

LOAD	Load accumulator
STO	Store
ADD	Add
SUB	Subtract
MULT	Multiply
DIV	Divide
GET	Input a value
PUT	Output a value
J	Jump
JN	Jump on negative
JP	Jump on positive
JNZ	Jump on negative or zero
JPZ	Jump on positive or zero
JNP	Jump on negative or positive
LAB	Label (no operation)
HALT	Halt execution

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PAM Example Translation

PAM program:

```

read x,y;
while x<= 99 do
  ans := (x+1) - (y / 2);
  write ans;
  read x,y
end

```

Translated machine code
assembly text

Low level representation
tree form

```

L1
GET x      STO T2
GET y      LOAD T1
LAB        SUB T2
LOAD x     STO ans
SUB 99     PUT ans
JZ L2      GET x
LOAD x     GET y
ADD 1      J L1
STO T1     LAB
LOAD y     HALT
DIV 2

MGET( I(x) )  MSTO( T(2) )
MGET( I(y) )  MLOAD( T(1) )
MLABEL( L(1) ) MB(MSUB,T(2))
MLOAD( I(x) )  MSTO( I(ans) )
MB(MSUB,N(99)) MPUT( I(ans) )
MJ(MJZ, L(2)) MGET( I(x) )
MLOAD( I(x) )  MGET( I(y) )
MB(MADD,N(1))  MJMP( L(1) )
MSTO( T(1) )   MLABEL( L(2) )
MLOAD( I(y) )  MHALT
MB(MDIV,N(2))

```

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Arithmetic Expression Translation Semantics

Beginning of RML Relation trans_expr:

```

relation trans_expr: Exp => Mcode list =
axiom trans_expr(INT(v)) => [MLOAD( N(v))]
axiom trans_expr(IDENT(id)) => [MLOAD( I(id))]
....

```

Code template for simple subtraction expression:

```

<code for expression e1>
MB(MSUB ( e2))
and in assembly text form:
<code for expression e1>
SUB e2

```

RML rule for simple (expr1 binop expr2):

```

rule trans_expr(e1) => cod1 &
  trans_expr(e2) => [MLOAD(operand2)] &
  trans_binop(binop) => opcode &
  list_append(cod1, [MB(opcode,operand2)]) => cod3
-----
trans_expr(BINARY(e1,binop,e2)) => cod3

```

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The Complete trans_expr Relation

```

relation trans_expr: Exp => Mcode list =
(* Evaluation of expressions in the current environment *)
axiom trans_expr(INT(v)) => [MLOAD( N(v))] (* integer constant *)
axiom trans_expr(IDENT(id)) => [MLOAD( I(id))] (* identifier id *)

(* Arith binop: simple case, expr2 is just an identifier or constant *)
rule
  trans_expr(e1) => cod1 &
  trans_expr(e2) => [MLOAD(operand2)] & (* expr2 simple *)
  trans_binop(binop) => opcode &
  list_append(cod1, [MB(opcode,operand2)]) => cod3
  -----
  trans_expr(BINARY(e1,binop,e2)) => cod3

(* Arith binop: general case, expr2 is a more complicated expr *)
rule
  trans_expr(e1) => cod1 &
  trans_expr(e2) => cod2 &
  trans_binop(binop) => opcode &
  gentemp => t1 &
  gentemp => t2 &
  list_append6(
    cod1, (* code for expr1 *)
    [MSTO(t1)], (* store expr1 *)
    cod2, (* code for expr2 *)
    [MSTO(t2)], (* store expr2 *)
    [MLOAD(t1)], (* load expr1 value into Acc *)
    [MB(opcode,t2)] ) => cod3 (* Do arith operation *)
  -----
  trans_expr(BINARY(e1,binop,e2)) => cod3
end (* trans_expr *)

```

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Help Relations Called from trans_expr Relation

```

relation trans_binop: BinOp => MBinOp =
axiom trans_binop(PLUS) => MADD
axiom trans_binop(SUB) => MSUB
axiom trans_binop(MUL) => MMULT
axiom trans_binop(DIV) => MDIV
end

```

```

relation gentemp: () => MTemp =

```

```

rule tick => no
-----
gentemp => T(no)

```

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Some Applications of RML



- Small functional language with call-by-name semantics (mini-Freja, a subset of Haskell)

Mini-Freja Interpreter performance compared to Centaur/Typol:

#primes	Typol	RML	Typol/RML
3	13s	0.0026s	5000
4	72s	0.0037s	19459
5	1130s	0.0063s	179365

- Almost full Pascal with some C features (Petrol)
- Mini-ML including type inference
- Specification of Full Java 1.2
- Specification of Modelica 2.0

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Additional Performance Comparison



Additional measurements on performed on a Fedora Core4 Linux machine (2007) with two AMD Athlon(TM) XP 1800+ processors at 1500 MHz and 1.5GB of memory

#primes	RML	SICStus	SWI	Maude MSOS Tool
8	0.00	0.05	0.00	2.92
10	0.00	0.10	0.03	5.60
30	0.02	1.42	1.79	226.7
40	0.06	3.48	3.879	-
50	0.13	-	11.339	-
100	1.25	-	-	-
200	16.32	-	-	-

Execution time in seconds. The - sign represents out of memory

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Some RML and Semantics References



- Web page, open source: www.ida.liu.se/~rml
- Adrian Pop and Peter Fritzson. *An Eclipse-based Integrated Environment for Developing Executable Structural Operational Semantics Specifications*. in *3rd Workshop on Structural Operational Semantics*. 2006. Bonn, Germany. Elsevier Science. Electronic Notes in Theoretical Computer Science (ENTCS) No:175, Issue 1. p. 71-75
- Mikael Pettersson, *Compiling Natural Semantics*. Lecture Notes in Computer Science (LNCS). Vol. 1549. 1999: Springer-Verlag. (Based on PhD Thesis at PELAB, Linköping Univ, 1995)
- Gilles Kahn, *Natural Semantics*, in *Programming of Future Generation Computers*, Niva M., Editor. 1988, Elsevier Science Publishers: North Holland. p. 237-258.

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Some Attribute-Grammar Based Tools



- JASTADD – OO Attribute grammars
- Ordered Attribute Grammars.
 - Uwe Kastens, Anthony M. Sloane
Generating Software from Specifications
2007 (c) Jones and Bartlett Publishers Inc.
www.jbpub.com

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Primarily Back-End Frameworks and Generators

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LCC (Little C Compiler) – Not really a Generator but uses IBURG



- Dragon-book style C compiler implementation in C
- Very small (20K Loc), well documented, well tested, widely used
- Open source: <http://www.cs.princeton.edu/software/lcc>
- Textbook *A retargetable C compiler* [Fraser, Hanson 1995] contains complete source code
- One-pass compiler, fast
- C frontend (hand-crafted scanner and recursive descent parser) with own C preprocessor
- Low-level IR
 - Basic-block graph containing DAGs of quadruples
 - No AST
- Interface to IBURG code generator generator
 - Example code generators for MIPS, SPARC, Alpha, x86 processors
 - Tree pattern matching + dynamic programming
- Few optimizations only
 - local common subexpr. elimination, constant folding
- Good choice for source-to-target compiling if a prototype is needed soon



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GCC 4.x

Not a Generator – but wide-spread usage

- Gnu Compiler Collection (earlier: Gnu C Compiler)
- Compilers for C, C++, Fortran, Java, Objective-C, Ada ...
 - sometimes with own extensions, e.g. Gnu-C
- Open-source, developed since 1985
- Very large
- 3 IR formats (all language independent)
 - GENERIC: tree representation for whole function (also statements)
 - GIMPLE (simple version of GENERIC for optimizations) based on trees but expressions in quadruple form. High-level, low-level and SSA-low-level form.
 - RTL (Register Transfer Language, low-level, Lisp-like) (the traditional GCC-IR) only word-sized data types; stack explicit; statement scope
- Many optimizations
- Many target architectures
- Version 4.x (since ~2004) has strong support for retargetable code generation
 - Machine description in .md file
 - Reservation tables for instruction scheduler generation
- Good choice if one has the time to get into the framework



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Open64 / ORC Open Research Compiler Framework

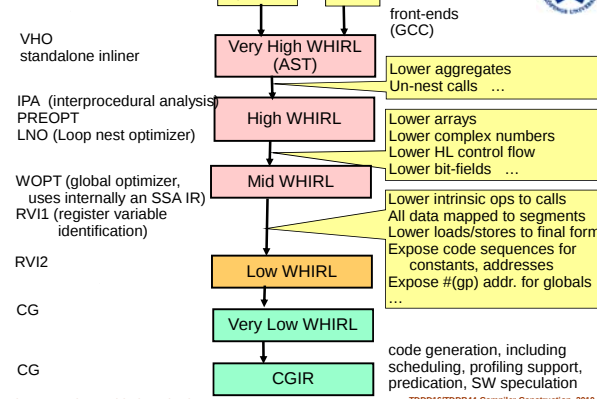
- Based on SGI Pro-64 Compiler for MIPS processor, written in C++, went open source in 2000
- Several tracks of development (Open64, ORC, ...)
- For Intel Itanium (IA-64) and x86 (IA-32) processors. Also retargeted to x86-64, Ceva DSP, Tensilica, XScale, ARM ... "simple to retarget" (?)
- Languages: C, C++, Fortran95 (uses GCC as frontend), OpenMP and UPC (for parallel programming)
- Industrial strength, with contributions from Intel, Pathscale, ...
- Open source: www.open64.net, ipf-orc.sourceforge.net
- 6-layer IR:
 - WHIRL (VH, H, M, L, VL) – 5 levels of abstraction
 - ▶ All levels semantically equivalent
 - ▶ Each level a lower level subset of the higher form
 - and target-specific very low-level CGIR

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ORC: Flow of IR



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Open64 / ORC Open Research Compiler

- Multi-level IR
 - translation by lowering
 - ☺ Analysis / Optimization engines can work on the most appropriate level of abstraction
 - ☺ Clean separation of compiler phases
 - ☹ Framework gets larger and slower
- Many optimizations, many third-party contributed components

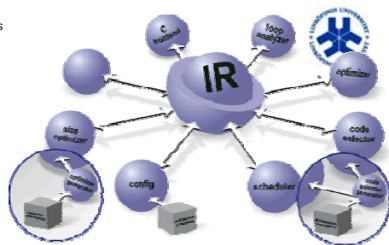
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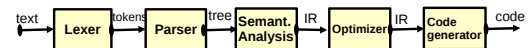
A commercial compiler framework
Primarily focused on backends

www.ace.ni

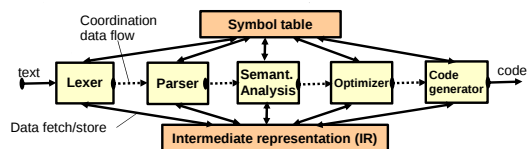
Peter Fritzson, Christoph Kessler,
IDA, Linköping universitet, 2010.

Traditional Compiler Structure

- Traditional compiler model: sequential process



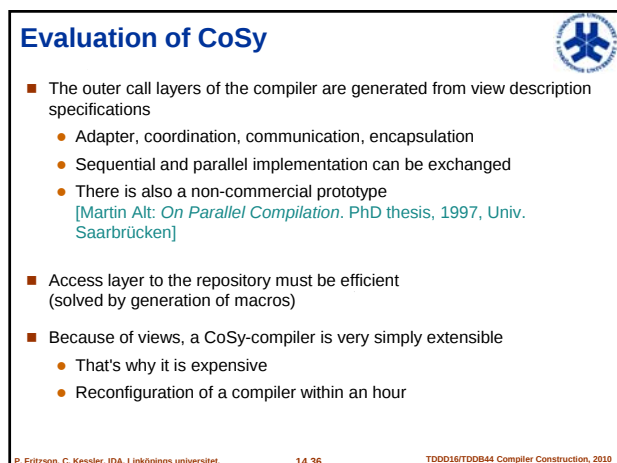
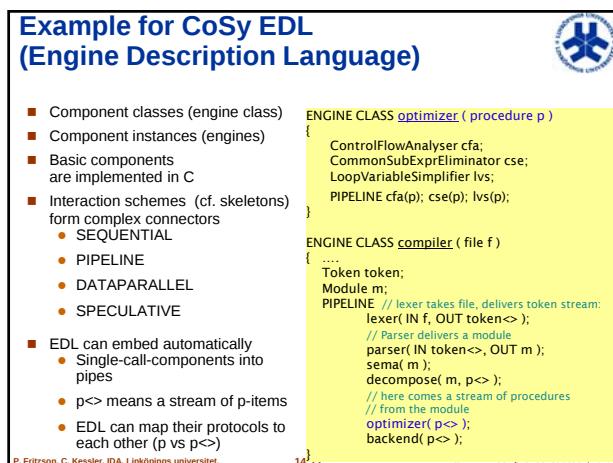
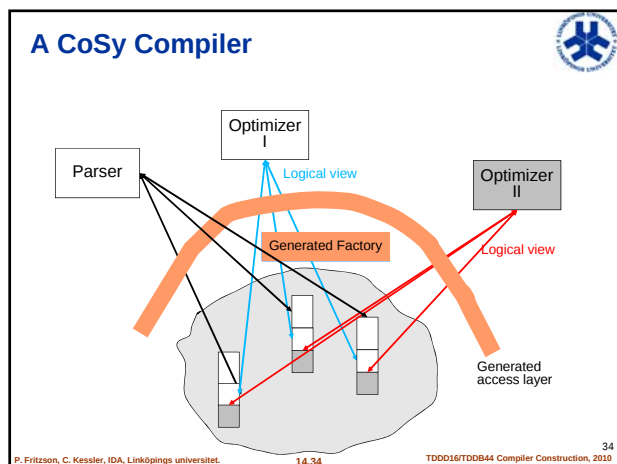
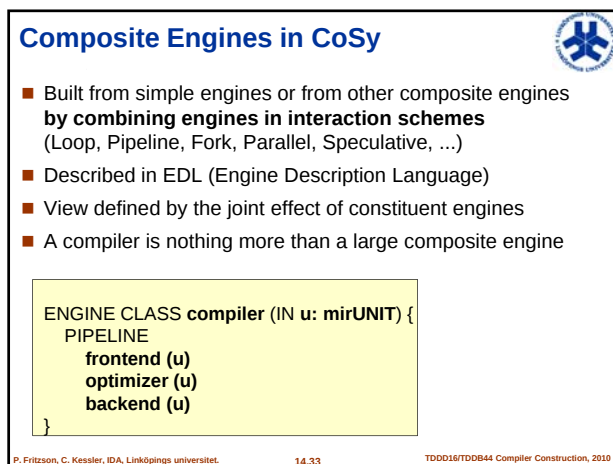
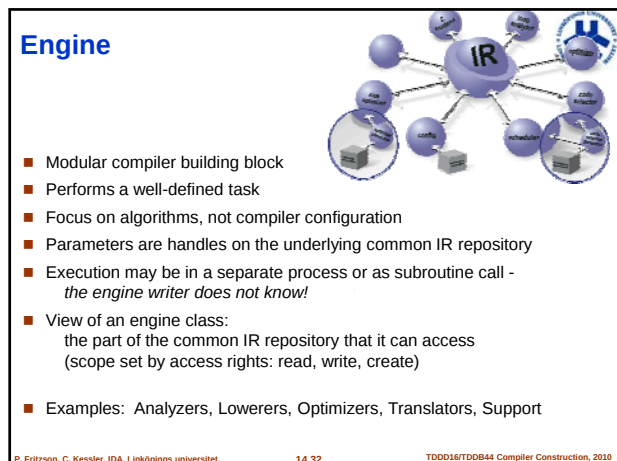
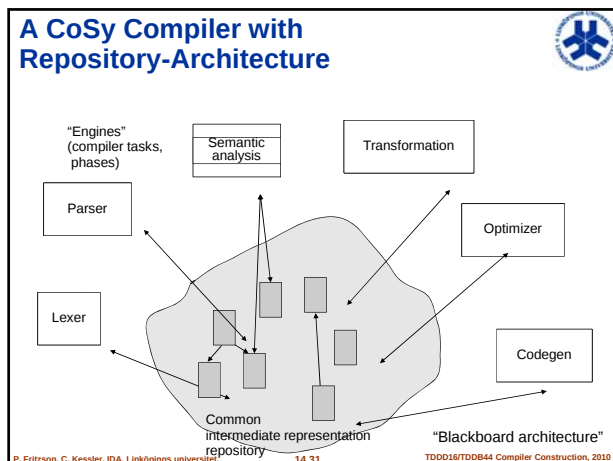
- Improvement: Pipelining (by files/modules, classes, functions)
- More modern compiler model with shared symbol table and IR:



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More Frameworks

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More Frameworks...



- **LLVM** (Univ. of Illinois at Urbana Champaign)
 - llvm.org
 - "Low-level virtual machine", IR
 - compiles to several target platforms: x86, Itanium, ARM, Alpha, SPARC
 - Open source
- **Cetus**
 - <http://cobweb.ecn.purdue.edu/ParaMount/Cetus/>
 - C/C++ source-to-source compiler written in Java.
 - Open source
- **Tools and generators**
 - TXL source-to-source transformation system
 - ANTLR frontend generator

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More frameworks...



- **Some influential frameworks of the 1990s**
 - **SUIF** Stanford university intermediate format, suif.stanford.edu
 - **Trimaran** (for instruction-level parallel processors) www.trimaran.org
 - **Polaris** (Fortran) UIUC
 - **Jikes RVM** (Java) IBM
 - **Soot** (Java)
 - GMD Toolbox / Cocolab **Cocktail**™ compiler generation tool suite
 - and many others ...
- And many more for the embedded domain ...

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The End (?)



"Now this is not the end.
It is not even the beginning of the end.
But it is, perhaps, the end of the beginning."
- W. Churchill

- Do you like compiler technology? Learn more?
 - TDDC86 Compiler optimizations and code generation 6hp
 - TDDC18 Component-based software 4.5hp
 - Thesis project (Exjobb) at PELAB, 30 hp

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