

AGENDA

Data: Thursday, Dec. 2, 1999

Time: 09.30 - 16.30

Place: Belöningen, Building B, at the main Campus of Linköping University.

Participants:

ESDlab/KTH:	Axel Jantsch Wenbiao Wu
ESLAB/LiTH	Zebo Peng Luis Alejandro Cortés Petru Eles
Saab	Lars Åke Classon Jan Erik Eriksson Fredrik Westman Torbjörn Månefjord Ingemar Söderquist Ove Åkerlund
Cybertech	Lars Ödman

Purpose: The purpose of the work-shop is to

- review the present results and status of the SAVE program,
- discuss and create consensus on important items and
- discuss and agree on short term work plans.

Agenda:

09.00 - 09.30	Arrival	
09.30 - 10.15	System design methodologies - Survey of design transformation techniques - A system design methodology based on the formal computational model	W Wu
10.15 - 10.40	Petri net modelling - Short introduction to Petri net modelling - Why is the Petri net modelling technique appropriate for SAVE?	P Eles
10.40 - 10.50	Pause	
10.50 - 11.30	Design representation modelling - Survey of HW/SW codesign representation models - A Petri net based model for heterogeneous electronic systems	L A Cortés

11.30 - 12.00	SAVE pilot application	I Söderquist
12.00 - 13.00	Lunch break	
13.00 - 14.30	Discussion of a tentative SAVE design flow - Sketch of a design flow based on the SAVE transformational approach. Links to Haskell, links to other tools?	L-Å Classon, F Westman, O Åkerlund, T Månefjord
14.30 - 15.00	Pro's and con's with Haskell in SAVE. Haskell course planning.	A Jantsch
15.00 - 15.15	Pause	
15.15 - 16.00	Short term work plans - ESDlab/KTH - ESLAB/LiTH - Saab	A Jantsch Z Peng I Söderquist
16.00 - 16.30	Wrap up, conclusions	J E Eriksson, Z Peng