

# Swedish INTELECT Summer School on Low Power Systems on Chip

August 23-25, 2004, Stora Hotellet, Örebro

## Preliminary program

### Monday, August 23, 08.30-21.30

9-10 Introduction, Christer Svensson  
10.30-12.30 Low Power RF, Sven Mattisson  
14-16 Low power DSPs, Christian Piguet  
16.30-18 Low power DSPs, Christian Piguet  
*Group discussions*

### Tuesday, August 24

8-10 High-level power modelling, Enrico Macii  
10.30-12.30 Power-efficient memory design, Enrico Macii  
14-16 Digital Circuits Design for Low-Power, Vojin Oklobdzija  
16.30-18 Digital Circuits Design for Low-Power, Vojin Oklobdzija  
*Dinner*

### Wednesday, August 25

8-10 Low power Baseband Processing, Dake Liu  
10.30-12.30 Low voltage and low power analog design, Dieter Draxelmayr  
14-16 AD and DA converters, Franz Dielacher  
16.30-18 Adiabatic logic, Lars Svensson