

Swedish INTELECT Summer School on Multiprocessor Systems on Chip

August 25-27, 2003, Stora Hotellet, Örebro

Final Program

Monday, August 25

- 8:30 Introduction
- 8:45 Reconfigurable Computing and its Compilation Techniques
Reiner Hartenstein, Kaiserslautern Univ. of Technology
- 10:30 Coffee
- 11:00 Distributed Memory and Datastream-based Reconfigurable Computing
Reiner Hartenstein, Kaiserslautern Univ. of Technology
- 13:00 Lunch
- 14:15 Predictable Performance in SoC and NoC
Kees Goossens, Philips Research
- 16:00 Coffee
- 16:30 Quality of Service in NoC
Kees Goossens, Philips Research
- 18:00 Break
- 18:30 Dinner
- 20:00 Group Discussions on Interesting Research Topics
- 21:30 End

Tuesday, August 26

- 8:30 Introduction to SoC Testing
Erik Jan Marinissen, Philips Research
- 10:30 Coffee
- 11:00 IEEE P1500 and Advanced SoC Test Architecture
Erik Jan Marinissen, Philips Research
- 13:00 Lunch
- 14:15 Life-Cycle Test Solutions & DFT for Telecom Applications
Gunnar Carlsson, Ericsson AB
- 16:15 Coffee

- 16:30 Optimized Design and Use of Memory Hierarchies in Data Intensive Multi-Media Applications
Per Gunnar Kjeldsberg, Norwegian Univ. of Science and Technology
- 18:30 Break
- 19:00 Dinner

Wednesday, August 27

- 8:30 NoC: State of the Art and Current Trends
Axel Jantsch, Royal Institute of Technology
- 10:30 Coffee
- 11:00 Three NoC Approaches: Aethereal NoC, SPIN, and Nostrum
Axel Jantsch, Royal Institute of Technology
- 13:00 Lunch
- 14:15 An Advanced Multiprocessor DSP System
Ulf Sjöström, Ericsson AB
- 16:15 Coffee
- 16:45 Final discussion
- 18:00 Dinner