

# **STRINGENT Workshop**

“Signal Processing Circuits and Systems for Communication”

Time: December 7, 2005, 13.15

Place: Alan Turing, IDA, Building E, First Floor (Level 3)

Organized by Håkan Johansson and Christer Svensson

## **Program**

13.15 Introduction (Christer Svensson)

13.30 Reconfigurable analog-to-digital converters (Per Löwenborg)

13.50 Reconfigurable DSP algorithms (Håkan Johansson)

14.10 Reconfigurable arithmetics and architectures (Oscar Gustafsson)

14.30 Break

14.50 Implementation of a programmable baseband processor (Eric Tell)

15.10 Acceleration in programmable multi-standard baseband processors (Anders Nilsson)

15.30 Programmable media processor (Di Wu)

15.50 Break

16.00 Switched-capacitor implementations of discrete-time filters (Jerzy Dabrowski and Stefan Andersson)

16.20 Estimation and synchronization algorithms for communication (Mattias Olsson)

16.40 Beer and sandwich