



STRINGENT Workshop

Topic: Low Power Design

Time: May 12, 2005, 13.15-17.00

Room: 'Glashuset' in B building, Linköping University

Organized by Atila Alvandpour and Christer Svensson

Program

13.15 Introduction (*A. Alvandpour*)

13.30 Energy-aware design of real-time embedded systems

Speaker: Alexandru Andrei

13.50 Low power media DSP design via memory transaction minimization

Speaker: Johan Eilert

14.10 Low power decoding of LDPC codes

Speaker: Anton Blad

14.30 Coffee Break

14.50 Power estimation for arithmetic operators

Speakers: Kenny Johansson and Oscar Gustafsson

15.10 Leakage tolerant circuit techniques

Speaker: Martin Hansson

15.30 1.56GHz On-chip Resonant Clocking with 2.3X Clock Power-Saving

Speakers: Martin Hansson and Behzad Mesgarzadeh

15.50 Break

16.00 A High Density, Low Leakage, 128Kb 5T-cell SRAM

Speaker: Stefan Andersson

16:20 Analog power consumption- An AD-converter case

Speaker: Christer Svensson

16.40 Beer and sandwich