

TDDE66/TDDD55 Organizational Issues

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Staff 2025

- ▶ Adrian Pop (AP)
 - ▶ Examiner (responsible for the courses and grading)
 - ▶ Lectures (second half)
- ▶ August Ernstsson (AE)
 - ▶ Lecturer
 - ▶ Lectures (first half)
- ▶ Jonathan Hjort (JH)
 - ▶ PhD student (20% teaching)
 - ▶ Labs TDDE66
- ▶ Amin Bajand (AB)
 - ▶ PhD student (20% teaching)
 - ▶ Labs and Lessons TDDD55
- ▶ Emilie Lind (EL)
 - ▶ Course administrator

Lecture Plan

- ▶ Fö1: Introduction (AP)
- ▶ Fö2 [optional for TDDE66]: Short introduction to formal languages ... (JW)
- ▶ Fö3 [optional for TDDE66]: ... and automata theory (AE)
- ▶ Fö4: Lexical analysis; Symbol tables (AE)
- ▶ Fö5: Parsing; Top-Down Parsing (AE)
- ▶ Fö6: Top-Down Parsing cont., Bottom-Up Parsing (intro) (AE)
- ▶ Fö7: Bottom-Up Parsing [LR(0) items optional for TDDD55] (AE)

Lecture Plan (cont.)

- ▶ Fö 8: Semantic analysis and internal forms. Syntax-driven translation. (AP)
- ▶ Fö 9: Memory management; run-time organization ... (AP)
- ▶ Fö10: Code optimization (AP)
- ▶ Fö11: Code generation (CG); general (AP)
- ▶ Fö12 [optional for TDDD55]: CG for RISC and superscalar processors (AP)
- ▶ Fö13: Error management. Interpreters (AP)
- ▶ Fö14: Bootstrapping. Compiler Generators. (AP)

Lessons / Tutorials

4x2h for TDDE66 and TDDD55

- ▶ Exercises on background theory (TDDD55)
- ▶ Preparation for the laboratory assignments (especially useful when we get a schedule where labs come before the lecture that covers the material 😞)
- ▶ Exam preparation session

Laboratory Assignments

- ▶ Separate for TDDE66 (3hp - 7 labs) and TDDD55 (2hp - 4 labs)
- ▶ Teams (subgroups) of size 2. TDDE66 - 1 group, TDDD55 - 1 group
- ▶ Register via WebReg (linked from the course home pages)
 - ▶ Remember to **register** to this course or WebReg can give you “funny” error messages.
 - ▶ It takes until the next working day for course registrations to be updated in WebReg.

Rules for examination of computer labs at IDA

You are expected to do lab assignments in group or individually, as instructed for a course. However, examination is always based on individual performance.

It is not allowed to hand in solutions copied from other students, or from elsewhere, even if you make changes to the solutions. If there is suspicion of such, or any other form of cheating, teachers are obliged to report it to the University Disciplinary Board.

Be prepared to answer questions about details in specific code and its connection to theory. You may also be asked to explain why you have chosen a specific solution.

This applies to all group members.

If you foresee problems meeting a deadline, contact your teacher. You can then get some help and maybe the deadline can be set to a later date. It is always better to discuss problems, instead of, e.g., to cheat.

Any kind of academic dishonesty, such as cheating, e.g. plagiarism or use of unauthorized assistance, and failure to comply with university examination rules, may result in the filing of a complaint to the University Disciplinary Board. The potential penalties include suspension, warning.

Policy for handing in computer lab assignments at IDA

For all IDA courses having computer lab assignments there will be one deadline during or at the end of the course. If you fail to make the deadline, you must retake the, possibly new, lab course the next time the course is given.

If a course deviates from this policy, information will be given on the course web pages.

https://www.ida.liu.se/edu/ugrad/lab_exam/

- ▶ 2025-12-21 for bonus points on the exam (TDDE66).
- ▶ Recommended last day to demonstrate and hand in final labs is the last scheduled lab. (Your teacher might be unavailable for example due to travelling for Christmas at later dates).
- ▶ If you have only 1 (TDDDD55) or 2 (TDDE66) labs remaining, it will be possible to correct the labs until the end of the exam period. After that you will generally need to wait until the next time the course is given.
- ▶ Extensions are given only for good reasons (typically a note from your doctor).

Literature

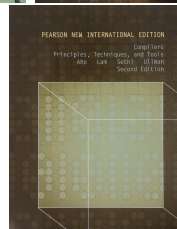
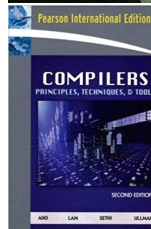
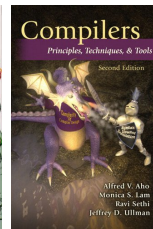
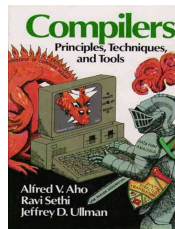
Mandatory (more or less):

- ▶ Aho, Lam, Sethi, Ullman: *Compilers – Principles, Techniques, and Tools*, Second Edition. Addison-Wesley, 2006.
 - ▶ 2007 version is a paperback that is the same (but published by Pearson International)
 - ▶ 2014 Pearson New International Edition is the same except the last chapter was magically removed. The book is larger, heavier, and more fragile than the older ones though.
 - ▶ The old first edition (1986) is still ok if you can find copies

Course homepage:

- ▶ Lab assignments
- ▶ Lecture notes

▶ Compiler construction exercises (exam preparation)



Exams

- ▶ Most assignments are the same for TDDE66 (3hp) and TDDD55 (2hp) except:
 - ▶ TDDD55: Formal languages / automata theory
 - ▶ TDDD55: Using an LR parser
 - ▶ TDDE66: RISC/CISC
- ▶ For TDDD55 students, the exam can be challenging without adequate preparation. There is an extra tutorial to prepare you for some of the harder exam assignments.
- ▶ For TDDE66, the lab series also prepares you for the exam since the labs are much more extensive than TDDD55. Our experience is that TDDE66 have an easier time preparing for the exam despite the assignments being more difficult.
- ▶ The TDDD55 exercises are split into 2 parts: fundamentals (which you must know) and advanced topics (which are used for higher grades), meaning you need to study fewer topics if you only want grade 3.
- ▶ The TDDE66 exercises give **3 bonus points** on the regular exam (not re-exams), if you pass the lab series before the deadline (end HT2 study period).

Course homepages

All information you need in the course are in your course home pages:

- ▶ <https://www.ida.liu.se/~TDDE66>
- ▶ <https://www.ida.liu.se/~TDDD55>

Note that Lisam will only be used for recorded lectures.
You still need to **register** to this course.

Course Evaluations

Year	TDDDB44			TDDDD55		
	Responses	Rating	Hours	Responses	Rating	Hours
2024-HT	17% (05 / 29)	4.00		14% (04 / 29)	3.25	
2023-HT	21% (10 / 47)	3.10		30% (03 / 10)	4.00	
2022-HT	27% (06 / 23)	4.00		32% (05 / 16)	2.00	
2021-HT	27% (06 / 25)	3.84		25% (03 / 12)	3.00	
2020-HT	24% (06 / 25)	4.00		17% (03 / 18)	2.70	
2019-HT	00% (00 / 16)	-		14% (03 / 21)	4.00	
2018-HT	28% (11 / 39)	4.36		00% (00 / 15)	-	
2017-HT	28% (09 / 32)	3.89	34.4	8% (01 / 13)	2.00	60.0
2016-HT	24% (08 / 34)	3.75	34.3	27% (03 / 11)	4.33	53.3
2015-HT	32% (10 / 31)	4.00	43.3	28% (07 / 25)	3.33	41.7
2014-HT	24% (10 / 41)	4.00	36.0	8% (02 / 26)	3.00	25.0

Note: Before 2018, Kurt was used instead of Evaluate.

Recent Course Changes

2025

- ▶ new lab assistant for TDDD55

2024

- ▶ Course code change TDDB44 -> TDDE66
- ▶ Small changes to the slides and the lab code (TDDD55)
- ▶ 1 lab group for TDDE66
- ▶ change of lab assistant for TDDE66

2022-2023

- ▶ Back to campus for everything, change in examiner
- ▶ Small changes to the slides and the lab code (TDDD55)
- ▶ 2 lab groups for TDDB44

2020, 2021

- ▶ Distance/Hybrid mode due to Covid-19

What comes after this course?

Join one of our compiler research teams at [pelab](#) and do a bachelor / master thesis project in compiler technology:

- ▶ Compiling for OO modeling languages (**OpenModelica**)
 - ▶ Compiler bootstrapping, international open source compiler, www.openmodelica.org (P. Fritzson, A. Pop, M. Sjölund)
 - ▶ Compilation on parallel machines (P. Fritzson, M. Gebremedhin)
 - ▶ Compiling for embedded systems (P. Fritzson, M. Sjölund)
 - ▶ We are also looking into the Julia programming language (J. Tinnerholm, A. Pop)
 - ▶ Debugger technology (P. Fritzson, M. Sjölund, A. Pop)
- ▶ Operational semantics based compiler generation (P. Fritzson, A. Pop)
- ▶ Code generation for instruction-level parallel and embedded processors (C. Kessler)
- ▶ Optimization problems in code generation (C. Kessler)
- ▶ Retargetability (C. Kessler)
- ▶ Program analysis and transformation, including automatic and semiautomatic parallelization (C. Kessler)

... and more

www.liu.se