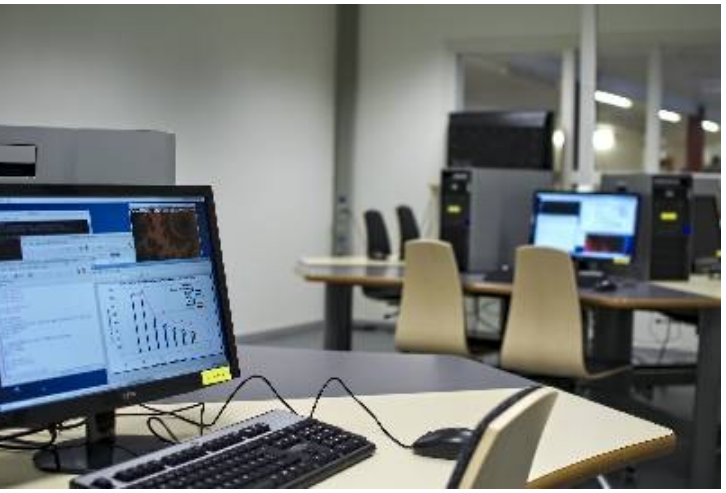
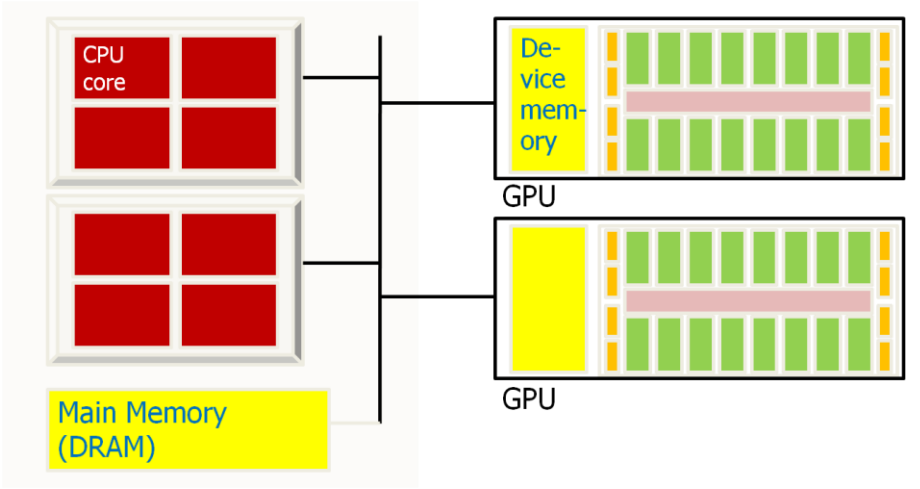




TDDD56
Multicore and GPU Programming

Christoph Kessler

IDA
Linköping University
Sweden

2025

LINKÖPING UNIVERSITY

Staff 2025

- **Christoph Kessler**, IDA christoph.kessler (at) liu. se
  - Organization, most lectures, examinator
- **Ingemar Ragnemalm**, ISY ingemar.ragnemalm (at) liu. se
  - Lectures on GPU programming, GPU labs
- **August Ernstsson, IDA** august.ernstsson (at) liu.se
  - Guest lecture in Lesson 2
- **Sajad Khosravi**, IDA sajad.khosravi (at) liu. se
  - Course assistant, lessons, labs
- **Sebastian Litzinger**, IDA sebastian.litzinger (at) liu. se
  - Additional lab assistant, CPU labs

- **Erika Larsson**, IDA erika.larsson (at) liu. se
  - Course secretary (Ladok reporting)
- **Martin Sjölund**, IDA martin.sjolund (at) liu. se
  - Director of undergraduate studies

Are you registered on TDDD56?

- Currently, the course (lab part) is **full**.
  - If taking the course this year,
    please register for the labs (webreg) by **friday** this week

- **Non-registered** LiU students
  who want to take the entire course (including the **labs**):
  - Please join the **waiting list** if not done yet.
  - Wait with Ladok and webreg registration
  - Notification about admission on sunday after the first week
  - No labs nor exam correction without valid course registration
  - Attending lectures and lessons is always possible

Course Moments

- Lectures
  - Transmitted on zoom for external participants on request
  - Not recorded
- Lessons
  - Lab introduction lessons, mandatory for the labs
  - Exam training lessons at the end of the course
- Labs (mandatory presence)

- Credits:
  - Written exam, 3 hp
  - Lab series attended and completed by deadlines, 3 hp
    - No guarantee for completing / correcting labs after the deadlines

Lectures (1)

- **Lecture 1:** Organization, Overview.
  Motivation, Multicore architectural concepts and trends (CK)
- **Lecture 2:** Shared memory architecture concepts* (CK)
- **Lecture 3a:** Parallel programming with threads (CK)
- **Lecture 4:** Non-blocking synchronization (CK)
- **Lecture 3b**: Parallel programming with tasks (CK, 45min)
  Lesson 1: CPU lab 1+2 introduction (SK, 45min)
- **Lectures 5-6:** Design and analysis of parallel algorithms* (CK)
- **Lecture 7:** Parallel sorting algorithms (CK)
- **Lecture 8**: Parallel algorithmic design patterns and skeletons. (CK, 45min)
  Lesson 2: Introduction to skeleton programming in SkePU / Lab 3 introduction (AE / SK, 45min)

* Similar as in TDDE65

Lectures (2)

…

- **Lecture 9:** GPU architecture and trends (IR)
- **Lecture 10:** Introduction to CUDA programming. (IR)
- **Lecture 11:** CUDA programming. GPU lab introduction. (IR)
- **Lecture 12:** Sorting on GPU. Advanced CUDA issues. (IR)
- **Lecture 13:** Introduction to OpenCL. (IR)
- **Lesson 3:** OpenCL. Shader programming. Exercises. (IR)
- **Lesson 4:** Selected theory exercises, exam training. (SK/CK)
- **Lecture 14:** Optimization and parallelization of loop-based sequential programs* (CK)

Lab Series (1)

NEW 2025

Lab 0: Programming in C and getting started with Pthreads

- week 45, only for those who have not taken a course in operating systems
- Focus on constructs perceived as difficult for Python / Java programmers
- mandatory, no examination
- Work through the C/Pthreads slide set before Wednesday!

CPU-labs (week 46, 47, 48)

- Lab 1: Load balancing (warm-up)
- Lab 2: ~~Nonblocking synchronization~~ Parallel sorting
- Lab 3: Skeleton programming; Median filtering

NEW 2025

GPU-labs (week 49, 50, 51)

- Lab 4: CUDA 1
- Lab 5: CUDA 2
- Lab 6: OpenCL and Shader programming

Lab Series (2)

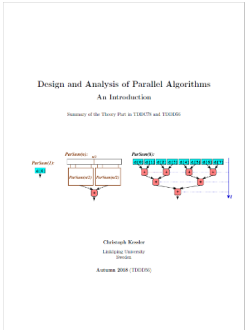
- 2 groups in 2 passes (A, B)
  - **Group A** (A1, A2), ~30 students in total
    - v45,46,47: Sajad Khosravi, Sebastian Litzinger
    - v48,49,50: Ingemar Ragnemalm, Sajad Khosravi
  - **Group B** (B1, B2), ~30 students in total
    - v46-50: Sebastian Litzinger, Sajad Khosravi
- We use the computers in ***Olympen*** (B-house, entry 25, upper floor)
- Work in **pairs**.
  - No singletons, please – the course is full this year
- Sign up in **webreg** (www.ida.liu.se/webreg) by **this friday**
  - We reserve the right to compact and balance lab groups

Lab Series (3)

- **Mandatory presence!** **(ISY-style labs)**
- The lab room (Olympen) is reserved (and paid!) for our course during scheduled lab hours.
  - No guarantees of access/availability of Olympen / its computers outside scheduled lab hours.
- Demonstration / lab reports to lab assistant by the **deadlines**
  - CPU labs: last CPU lab session **26/11** resp. **28/11/2025** (soft), latest **17/12** resp. **18/12/2025** (hard)
  - GPU labs: last GPU lab session, **17/12** resp. **18/12/2025**
- Be well-prepared!
  Supervised lab time is too costly for reading the instructions …
- **No copying!**

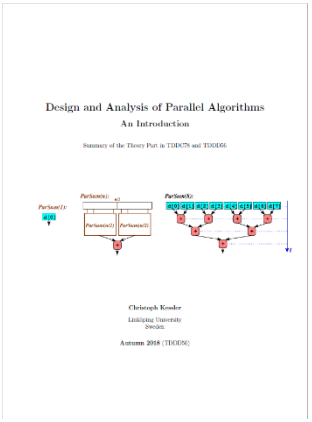
Changes Since 2024

- No major changes in contents or structure
  - Course evaluations use to be at/above 4
- Now open for additional programs/profiles
  - 2025: Technical Mathematics, computer vision
- Lab 0 added
  - to help students from programs without a mandatory operating systems course (media engineering, computer vision)
- Lab 2 redesigned from scratch
  - Nonblocking synchronization → Parallel sorting
- Three new chapters in the compendium book
  - Loop optimization and parallelization (2024)
  - Parallel computer architecture concepts (2024)
  - Thread programming (2025)



Course material and homepage

- All information available on the **course homepage:** www.ida.liu.se/~TDDD56
  - We do *NOT* use LISAM!
- **Course books / compendia**:
  - C. Kessler: ***Design and Analysis of Parallel Algorithms: An Introduction***. Compendium, (c) 2024.
    - PDF available for registered course participants. Not for general distribution.
    - Covers the lectures on analysis of parallel algorithms and on parallel sorting, parallel architecture concepts, loop parallelization, and the patterns introduction.
  - I. Ragnemalm: ***Attack in Packs***. Compendium, (c) 2018.
    - PDF available for registered course participants. Not for general distribution.
    - Covers the GPU lectures.
- Some slide sets and other material require **login/password**
  - Sent out to registered participants + guest participants
  - Please keep it secret
- **Lab assignments** on the course homepage

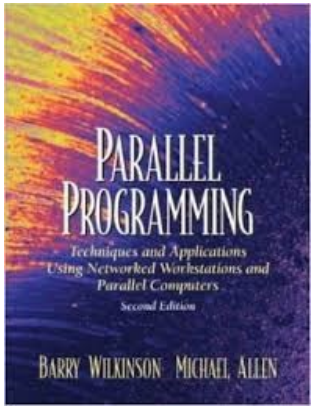


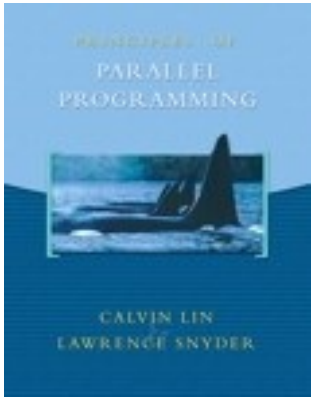
Introductory Literature (Selection)

If you already attended TDDE65, you need no other textbook on the general parallel computing / CPU part.

Otherwise, one of the following introductory books might be useful (available in the LiU library as refcopy and for loan):

- B. Wilkinson, M. Allen:
  Parallel Programming, 2e.
  Prentice Hall, 2005.
  (general introduction; **Pthreads**, OpenMP, MPI;
  also used in TDDE65)
- C. Lin, L. Snyder:
  Principles of Parallel Programming.
  Addison Wesley, 2008.
  (general introduction; **Pthreads**)
  - Errata for the first printing:
    https://www.cs.utexas.edu/~lin/errata.html



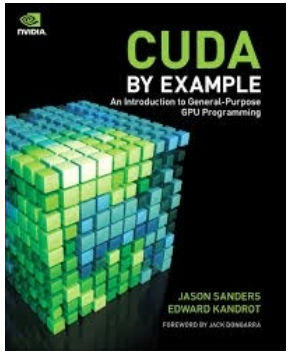


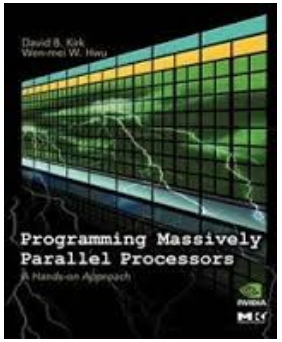
GPU Programming Literature

Focus on CUDA.

One of the following books might be useful:

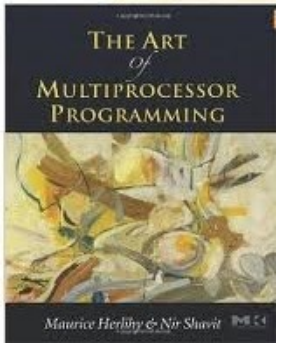
- J. Sanders, E. Kandrot: *CUDA by example*. Addison-Wesley, 2011. (recommended)
- David B. Kirk and Wen-mei W. Hwu: *Programming Massively Parallel Processors: A Hands-on Approach.* Morgan Kaufmann, 2010. Second edition 2012. Third edition 2016. Fourth edition 2022.

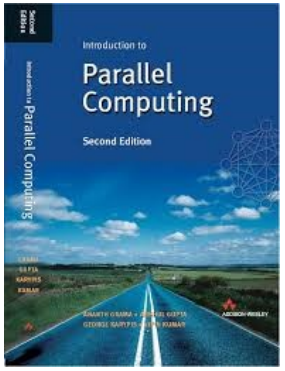




Available in the LiU library

Further Reading





- M. Herlihy, N. Shavit: *The Art of Multiprocessor Programming*. Morgan Kaufmann, 2008.
  (threads; nonblocking synchronization)
- A. Grama, G. Karypis, V. Kumar, A. Gupta: *Introduction to Parallel Computing, 2nd Edition.* Addison-Wesley, 2003.
  (design and analysis of parallel algorithms)
- …

See the course homepage for further references

- Available in the LiU library

- **On-line references** on the course homepage

Another Master-Level Course …

TDDE65 Programming of Parallel Computers, 6hp

- VT2 (March–May) every year
- Topics include:
  - Parallel computer architecture concepts, esp. clusters
  - Parallel algorithms for High-Performance Computing
  - Parallel thread programming with OpenMP **(Labs)**
  - Message passing programming of clusters with MPI **(Labs)**
  - Tools for performance analysis (**Labs**)
- Labs on Swedens largest (academic) supercomputer (or equivalent), at NSC
- A good complement of TDDD56